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Date: June 15, 2005

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TRANSMITTAL		Filing Date	January 28,	January 28, 2004	
FORM		First Named Inventor	Badredin Fatemizadeh		
(to be used for ell correspondance after initial titing)		Art Unit	2826		
		Examiner Name Attorney Docket Number		Wilson, Scott R.	
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Firm or Robert D. Atkins R	leg. 34,2	288 - QUARLES & BRADY	STREICH LA	NG LLP	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Badredin Fatemizadeh et al.

Serial Number

10/767,384

Date of Filing

January 28, 2004

Title

LOW COST DIELECTRIC ISOLATION METHOD

FOR INTEGRATION OF VERTICAL POWER

MOSFET AND LATERAL DRIVER DEVICES

Art Unit

2826

Examiner

Wilson, Scott R.

Confirmation No.

4751

USPTO Cust. No.

26707

Attorney Docket No.:

112518.00006

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed June 3, 2005, Applicant hereby elects, without traverse, the claims classified as Group I in the Office Action. Namely, Applicant elects Claims 1-19, drawn to a method, classified in class 438, subclass 221.

Applicant hereby withdraws Claims 20-24 from further consideration in this Office Action, but reserves the right to prosecute these claims in one or more divisional applications, which may be filed before the close of prosecution of the instant application.

Respectfully submitted,

OUARLES & BRADY STREICH LANG LLP

June 15, 2005

Robert D. Atkins

Reg. No. 34,288

Attorney Docket No. 112518.00006

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